

AMENDMENT TO THE SPECIFICATION

Please replace paragraph 0050 beginning on line 7 of page 10 as follows:

Referring to FIG. 11 with continued reference to FIG. 10, there is schematically illustrated the functional components of digital processor 216. The digital processor 216 comprises spread code generator 252, and ~~correlator correlator~~ 254. Correlator 254 receives input signal 256 from ADC 214 and spread code signal 218 from spread code generator 252. Correlator 254 time aligns and despreads signal 256. In the illustrated implementation, the spread code generator 252 comprises a frequency hopped spread spectrum (FH-SS) code generator that provides an FH-SS spreading code 264 and a direct sequence spread spectrum (DS-SS) spreading code generator 266 that provides a DS-SS spreading code 268. Spread code signal 218 is also provided to DAC 220 to provide spread code to spread the analog IF signal from the mixer 208. It will be appreciated the spread code signal 218 can include both the FH-SS spreading code 264 and the DS-SS spreading code 268 for use in spreading the analog IF signal at the one or more mixers 212. Although FIG 11 illustrates the digital processor 216 having a spread signal code generator 252 and correlator 254, those skilled in the art can readily appreciate, any or all of these functions may be performed by circuits external to the digital processor 216, and/or software algorithms executed by the digital processor 216, and the examples presented herein should not be construed as limited to these functions being performed by a digital processor. Alternatively, a time delay (not shown) can be used to synchronize the spreading and despreading circuits which may eliminate the need for correlator 254 to perform the time alignment.